

PATENT

ATTORNEY DOCKET NO.: Intel 2207/793203  
ASSIGNEE: INTEL CORPORATION

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS : Amit A. MERCHANT et al.

SERIAL NO. OF  
PARENT APPLICATION : 10/060,264

FILING DATE OF  
PARENT APPLICATION : February 1, 2002

FOR : STORING OF INSTRUCTIONS RELATING TO A  
STALLED THREAD

GROUP ART UNIT OF  
PARENT APPLICATION : 2154

EXAMINER OF  
PARENT APPLICATION : Larry D. DONAGHUE

M/S: PATENT APPLICATION  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Certificate of Mailing**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: M/S: PATENT APPLICATION, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

Date: March 2, 2004

Signature *Blanche Guzman-Salmon*  
Blanche Guzman-Salmon

**INFORMATION DISCLOSURE STATEMENT**

Dear Sir:

Submitted herewith is an Information Disclosure Statement under 37 CFR §1.97(b)(3) with twenty one (21) reference(s) listed on the attached PTO-1449 (modified) form. Copies of each of the references were previously submitted or cited in one of the parent Application Nos. 10/060,264, filed February 1, 2002, 09/848,423, filed May 4, 2001 and 09/474, 082, filed December 29, 1999. A copy of the cited references is not enclosed since the present application

was filed after June 30, 2003. (See OG Dated: August 5, 2003).

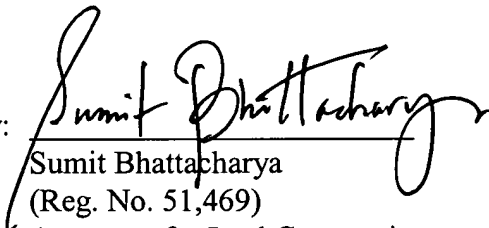
It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

If any fee is required, the Office is hereby authorized to charge any fees, or credit any overpayments under 37 CFR §1.17(p) to Kenyon & Kenyon, Deposit Account No. 11-0600.

Respectfully submitted,

KENYON & KENYON

Dated: March 2, 2004

By:   
Sumit Bhattacharya  
(Reg. No. 51,469)  
Attorneys for Intel Corporation

KENYON & KENYON  
333 West San Carlos St., Suite 600  
San Jose, CA 95110

Telephone: (408) 975-7500  
Facsimile: (408) 975-7501

<b>Form PTO-1449 (modified)</b>  <b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT(S)' INFORMATION DISCLOSURE STATEMENT</b>	<b>Attorney Docket No.</b>  2207/793203	<b>Serial No. of Parent Application</b>  10/060,264
	<b>Applicants</b> Amit A. MERCHANT et al.	
	<b>Filing Date of Parent Application</b> February 1, 2002	<b>Group Art Unit of Parent Application</b> 2154

**U. S. PATENT DOCUMENTS**

EXAMINER INITIAL	PATENT NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE
	5,185,871	February 9, 1993	Frey et al.	712	205	December 26, 1989
	5,197,132	March 23, 1993	Steely, Jr. et al.	395	375	June 29, 1990
	5,341,482	August 23, 1994	Cutler et al.	712	244	December 22, 1992
	5,519,841	May 21, 1996	Sager et al.	711	202	November 12, 1992
	5,828,868	October 27, 1998	Sager et al.	395	556	November 13, 1996
	5,835,745	November 10, 1998	Sager et al.	395	391	March 7, 1996
	5,966,544	October 12, 1999	Sager	712	32	November 13, 1996
	6,088,790	July 11, 2000	Grochowski	712	218	December 19, 1997
	6,094,717	July 25, 2000	Merchant et al.	712	32	July 31, 1998
	6,163,838	December 19, 2000	Merchant et al.	712	219	June 30, 1998
	6,542,921	April 1, 2001	Sager	709	108	July 8, 1999
	6,212,544	April 3, 2001	Borkenhagen et al.	709	103	October 23, 1997
	6,507,862	January 14, 2003	Joy et al.	709	107	May 11, 1999
	6,535,905	March 18, 2003	Kalafatis et al.	709	108	March 18, 2003

\*\*A copy is not enclosed since the present application was filed after June 30, 2003. (See OG Dated: August 5, 2003)

**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

**OTHER DOCUMENTS**

EXAMINER INITIAL	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
	Wallace et al., Threaded Multiple Path Execution, June 1998, pp.1-12.
	Roos et al., Execution Replay of Parallel Programs, 1993, pp. 1-6.
	Marcuello et al., Data Speculative Multithreaded Architecture, 1998, pp. 321-324.
	Gulati et al., Performance Study of a Multithreaded Superscalar Microprocessor, 1996, pp. 291-301.
	Akkary et al., A Dynamic Multithreading Processor, 1998, pp. 226-236.
	Tsai et al., Performance Study of a Concurrent Multithreaded Processor, 1998, pp. 24-35.
	Loikkanen et al., A Fine-Grain Multithreading Superscalar Architecture, 1996, pp. 163-168.

EXAMINER	DATE CONSIDERED
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	